adume 2, Number 1, anuary-June 2014

## Indian Journal of Electronic & Tele Communications Engineering

Editor-in-Chief Professor Marwan Al-Akaidi

GBS PUBLISHERS & DISTRIBUTORS (I)

## Indian Journal of Electronic & Tele Communications Engineering

Volume 2, Number 1 (2014)

Contents	
A FPGA-Based Implementation of an 802.11a Transmitter Rana Mukherji, T.R. Choudhary and Amit Kumar Chatterjee	1–6
<b>Compression of Image using 2D-DWT Based on Lifting Scheme</b> Namrata Jaswal, Amanjeet Panghal and Anil Garg	7–20
Reduced Volterra Series (RVS) Based Modeling of Non-Linear Power Amplifier with Memory Effects for Digital Pre-Distortion Applications Amandeep Singh Sappal, Manjit Singh Patterh and Sanjay Sharma	21–32
<b>D-Statcom Based Voltage Regulation and Harmonic Damping</b> <b>D.</b> Kalyankumar and V. Kirubakaran	33-42
Generation of Asymmetric Sum and Difference Patterns from Arrays of Dipoles and Arrays of Waveguides M. Satyanarayana and G.S.N. Raju	43–55
Wideband Rectangular Patch Antenna L Lolit Kumar Singh, Bhaskar Gupta and Partha P. Sarkar	57-62
Reduction of Sidelobes of Radiation Patterns in the Uniformly Excited Arrays K.V.S.N. Raju and G.S.N. Raju	63-83