Seat No:_____

PARUL UNIVERSITY

FACULTY OF IT & COMPUTER SCIENCE MCA Winter 2019 – 20 Examination

Semester: 1 Date:29/11/2019

Subject Code: 05201103 Time:02:00 pm to 04:30 pm

Subject Name: Fundamentals of Computer Organization Total Marks: 60

Q.1 A.	Answer the followings. Do as directed.		(05)
110	1. ALU stands for		(02)
	2. Explain term RAM?	 -	
	3. List various types of RO	M.	
	4. What is Cache Memory.		
	5. $(5238)_{10} = (?)_2$		
В.	Multiple choice type questi	(10)	
	1 is indicate distributive law.		
	A. A(B+C) = AB + AC	C. A(B+C) = (A+B)C	
	B. (AB)C = A(BC)	D. $AB=BC \Rightarrow A=C$	
	2. Which memory device is §		
	A. RAM	C. Hard-disk	
	B. Floppy disk	D. CD disk	
		ary of the decimal problem $4*8 = ?$	
	A. 101010	C. 100000	
	B. 001111	D. 110011	
	4. Fastest memory access e		
	A. Cache	C. RAM	
	B. Accumulators	D. Hard Disk	
	5. $(6)_{10} = (?)_2$	~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~	
	A. 0011	C. 0110	
	B. 1010	D. 1111	
	6. $(0101)_2 = ()_8$	G 21	
	A. 10	C. 21	
	B. 5	D. 8	
	7. $(1100 \ 1110)_2 = (?)_{16}$	0.05	
	A. CE	C. C5	
	B. A9	D. BC	
		expression is in sum-product (SOP) form?	
	A. $(A+B)(C+D)$		
	B. (AB)+(CD)9. Full form of CPU	D. AB	
	A. Central Processing U		
	B. Control Programming		
	10 is used to store		
	A. D flip flop	B. JK flip flop	
	C. RS flip flop	D. None of the mentioned	
Q.2	Answer the followings.	D. None of the mentioned	(15)
Q.2	1. Explain basic laws of Bo	(2)	
	2. Explain Demorgan's theorem.		(2)
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	6. Explain instruction form	at with diagram.	(3)

Q.3	Answer the following. (Any three)		
	1. Explain Immediate and Register addressing modes.		
	2. What is multiplexer? Explain 8 x 1 multiplexer with diagram and truth table.		
	3. List out different types of bus. Explain any two bus in detail.		
	4. Write a note on Direct and Register indirect addressing modes.		
Q.4	Answer the following.		
A.	List all optical storage devices. Explain any one in detail.		
B.	Explain instruction Set of Intel 8086 microprocessor and Draw architecture of Intel 8086		
	processor.		
	OR		
B.	Explain following instructions:		
	(a) MOV (b) ADD (c)DEC (d) CMP (e) AND (f)NOT (g) ROL (h) OR (i) NEG (j) DIV		