Seat N	No:	Enrollment No:
Scat I	PARUL UNIVERSITY	
	FACULTY OF ENGINEERING & TEC	
	B.Tech./Int. Btech Summer 2022 - 23 E	
Seme	Semester: 4/3/8 Date: 27/03/2023	
		Time: 02:00 pm to 04:30 pm
•	ject Name: Computer Organization and	Total Marks: 60
·	Microprocessor Architecture	
Instr	ructions:	
1. Al	ll questions are compulsory.	
	gures to the right indicate full marks.	
	ake suitable assumptions wherever necessary.	
4. Sta	art new question on new page.	
Q.1	 Objective Type Questions - (Fill in the blanks, one word answer case of MCQ) (All are compulsory) (Each of one mark) What is the size of data bus in 8085 microprocessor? How many bits are not used in flag register? What is machine cycle? What is an Assembler? The operation code of ADD is 10000000. Find the Hex code Calculate the number of chips required to design 8K-byte me 1024 x 1. Explain OUT instruction. What is partial decoding of I/O devices? Assume the accumulator holds FFH. Illustrate the difference incrementing the accumulator contents. 	instruction ADD B. mory if the memory chip size is
Q.2	 Explain DCX instruction. What is a counter? Explain RRC instruction. Which instruction is used to mask RST 7.5, 6.5, and 5.5 inter. What is a micro-operation? Describe the control function with example in register transfet. Answer the following questions. (Attempt any three) Explain the flag register in detail. Differentiate between peripheral-mapped I/O and memory-m. Draw and explain accumulator bit pattern of RIM Instruction Sixteen bytes of data are stored in memory locations at 2050F 	r language. (15) apped I/O.

transfer the entire block of data to new memory locations starting at 2070H.

B) Write an assembly language program to count the number of odd and even numbers from a

B) Write an assembly language program to add the positive numbers from a block of data having

ten signed numbers from memory location 4000H to 4009H and display sum if less than FFH,

Q.3 A) Draw and explain timing diagram for MVI C, CCH.

if not then display FFH.

B) Explain different types of memory.

block data having ten bytes from 2000H to 2009H.

Q.4 A) Explain the stack in detail with role of PUSH and POP instructions.

A) Describe shift micro operations with hardware implementation.

(07)

(08)

(08)

(07)

(07)

(08)