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## PARUL UNIVERSITY

## FACULTY OF ENGINEERING \& TECHNOLOGY B.Tech./Int. Btech Summer 2022-23 Examination

Semester: 4/3/8
Subject Code: 203124209
Subject Name: Computer Organization and
Microprocessor Architecture

## Instructions:

1. All questions are compulsory.
2. Figures to the right indicate full marks.
3. Make suitable assumptions wherever necessary.
4. Start new question on new page.
Q. 1 Objective Type Questions - ( Fill in the blanks, one word answer, MCQ-not more than Five in case of MCQ) (All are compulsory) (Each of one mark)
5. What is the size of data bus in 8085 microprocessor?
6. How many bits are not used in flag register?
7. What is machine cycle?
8. What is an Assembler?
9. The operation code of ADD is 10000000 . Find the Hex code instruction ADD B.
10. Calculate the number of chips required to design 8 K -byte memory if the memory chip size is $1024 \times 1$.
11. Explain OUT instruction.
12. What is partial decoding of I/O devices?
13. Assume the accumulator holds FFH. Illustrate the difference in flag set by adding 01 H and by incrementing the accumulator contents.
14. Explain DCX instruction.
15. What is a counter?
16. Explain RRC instruction.
17. Which instruction is used to mask RST $7.5,6.5$, and 5.5 interrupts?
18. What is a micro-operation?
19. Describe the control function with example in register transfer language.
Q. 2 Answer the following questions. (Attempt any three)
A) Explain the flag register in detail.
B) Differentiate between peripheral-mapped I/O and memory-mapped I/O.
C) Draw and explain accumulator bit pattern of RIM Instruction.
D) Sixteen bytes of data are stored in memory locations at 2050 H to 205 FH . Write instructions to transfer the entire block of data to new memory locations starting at 2070H.
Q. 3 A) Draw and explain timing diagram for MVI C, CCH.
B) Write an assembly language program to count the number of odd and even numbers from a block data having ten bytes from 2000 H to 2009 H .

OR
B) Write an assembly language program to add the positive numbers from a block of data having ten signed numbers from memory location 4000 H to 4009 H and display sum if less than FFH , if not then display FFH.
Q. 4 A) Explain the stack in detail with role of PUSH and POP instructions.

OR
A) Describe shift micro operations with hardware implementation.
B) Explain different types of memory.

