Enrolment Number:

PARUL UNIVERSITY FACULTY OF ENGINEERING & TECHNOLOGY **B.TECH MID SEM EXAMINATION** 3RD SEMESTER ACY-2022-23 (ODD SEM)

Subject Name: Electrical Circuit Analysis (203106205) **Branch: Electrical Engineering** Time: 02:30 PM-04:00 PM **Total Marks: 40** Date: 05/08/2022

Sr. No.

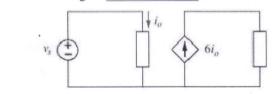
Marks 05

Q.1

(A)

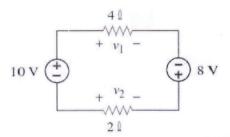
1. Find current flowing in a dielectric material due to a charge of 60 C after 12 s.

2. The dependent source in fig. is

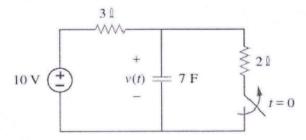


3. Find V_2 in the circuit of figure.

A

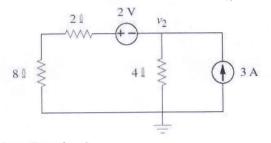


4. In the circuit of figure, find the voltage drop across 2 Ω just before t = 0.

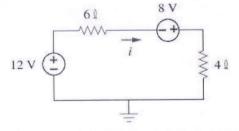


5. Obtain time constant for an series R-C circuit with $R = 2 \Omega$ and $C = 10 \mu F$. (B) Calculate power delivered by 3 A in the below circuit using nodal analysis.

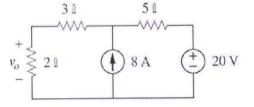
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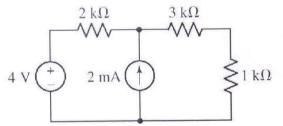
- Q.2 Attempt any four(Short Questions)
 - (1) Evaluate voltage drop across 6 Ω resistor in the below circuit.



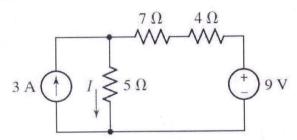
(2) Using the superposition theorem, find V_0 in the circuit in figure.



(3) Find the venin's equivalent circuits for given network faced by the 3 k Ω resistor.



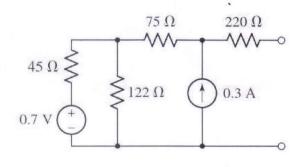
(4) Determine the current 'I' in the circuit of figure by performing source transformations



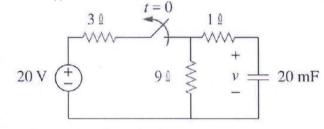
(5) Employ Norton's theorem to obtain a simple two-component equivalent of the circuit shown in figure.

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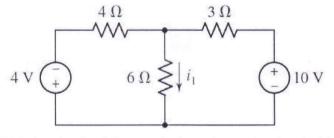


- Q.3 Attempt any two
 - (1) The switch in the circuit in figure has been closed for a long time, and it is opened at t = 0. Find v(t) for t = 0.

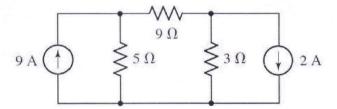


(2) Verify Tellegen's theorem in below circuit.

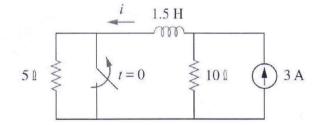
A



(3) With reference to the circuit of figure calculates the power absorbed by the 9 Ω resistor.



Q.4 (A) The switch in figure has been closed for a long time. It opens at t = 0. Find i(t) for t>0.



(B) Derive the condition for maximum power transfer from source to load in given

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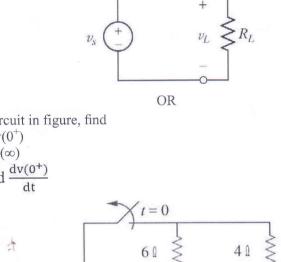
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R.

iL

i

2 H ਤੋ

+

 \mathcal{V}

0.4 F

(B) For the circuit in figure, find (a) $i(0^+)$ and $v(0^+)$ (b) $i(\infty)$ and $v(\infty)$ (c) $\frac{di(0^+)}{dt}$ and $\frac{dv(0^+)}{dt}$

12 V (+

circuit